L	Hits	Search Text	DB	Time stamp
Number 1	5	29/832.ccls. and solder near paste and	USPAT;	2004/06/24
		cream	US-PGPUB;	10:38
			EPO; JPO;	
			DERWENT;	
_			IBM_TDB	
2	73	module adj connection and pcb and ball	USPAT;	2004/06/24
			US-PGPUB;	10:45
			EPO; JPO;	
			DERWENT;	
2	_	module editormostica and est and bell and	IBM_TDB	2004/06/24
3	3	module adj connection and pcb and ball and	USPAT;	2004/06/24
		29/\$.ccls.	US-PGPUB;	10:46
			EPO; JPO;	
			DERWENT;	
4	18	module adj connection and pcb and ball and	IBM_TDB USPAT;	2004/06/24
-	'8	(29/832; 29/833 ; 29/840 ; 29/834 ; 29/740 ;	US-PGPUB;	10:47
		29/841; 29/854; 29/855; 29/DIG.1; 174/176;	EPO; JPO;	10.47
		174/189; 174/52.1; 228/178	DERWENT;	
		; 228/179.1 ; 228/180.1 ; 361/760 ;	IBM_TDB	
		361/764 ; 361/719	IBW_IBB	
		; 257/697 ; 257/737 ; 257/739 ;		
		257/777-779).ccis.		
5	29	module adj connection and pcb and ball and	USPAT;	2004/06/24
		(29/832; 29/833 ; 29/840 ; 29/834 ; 29/740 ;	US-PGPUB;	10:55
		29/841; 29/854; 29/855; 29/DIG.1; 174/176;	EPO; JPO;	10.00
		174/189 ; 174/52.1 ; 228/178	DERWENT;	
		; 228/179.1 ; 228/180.1 ; 361/760 ;	IBM_TDB	
		361/764 ; 361/719		
		; 257/697 ; 257/737 ; 257/739 ;		
		257/777-779; 361/760, 257/664 , 257/724 ,		
		257/728 , 257/924 , 333/246		
		, 333/247 , 361/780 , 361/782 ,		
		361/783).ccls.		
6	0	Bourrierer near francis .inv.	USPAT;	2004/06/24
			US-PGPUB;	10:56
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
7	51	Bourrieres near francis .inv.	USPAT;	2004/06/24
			US-PGPUB;	11:01
			EPO; JPO;	
			DERWENT;	
_			IBM_TDB	
8	6	("5346118" "5539153" "5587342"	USPAT	2004/06/24
		"5658827" "5872051" "5934545").PN.		10:59
9	0	Bourrieres near francis .inv. and 29/832.ccls.	USPAT;	2004/06/24
			US-PGPUB;	11:02
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

10	1	Bourrieres near francis .inv. and module and	USPAT;	2004/06/24
		interconnection	US-PGPUB;	11:03
			EPO; JPO;	
			DERWENT;	
			IBM TDB	

	Title	Current OR
1	Method of forming an electrical interconnect	438/125
2	Ball placement method and apparatus for forming a ball grid array	228/191
3	Process for transferring material to semiconductor chip conductive pads using a transfer substrate	438/616
4	Method for forming solder balls on a substrate	228/180.22
5	Method of bumping substrates by contained paste deposition	174/260
6	Surface mount solder assembly of leadless integrated circuit packages to substrates	228/180.22

	Current XRef	
1	257/E21.508; 257/E23.021; 430/315; 438/613; 438/96	
2	228/246; 228/254; 228/41; 257/E21.508	
3	228/180.22; 257/E21.503; 257/E21.508; 257/E21.511; 257/E23.021; 257/E23.061; 257/E23.067; 257/E23.068	
4	427/96; 438/125; 438/615	
5	174/259; 228/180.1; 257/E21.503; 257/E21.508; 257/E23.021; 257/E23.069; 29/832	
6	228/248.1; 228/254; 257/E21.511; 427/191; 427/383.7	